

Amendment Under 37 C.F.R. § 1.116  
U.S. Serial No. 09/898,185  
Group Art Unit: 2674  
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IN THE CLAIMS:

1. (Currently Amended) A drive circuit for use in a liquid crystal display, said drive circuit supplying source signal from a source driver to pixel electrodes through switching by means of thin film transistors according to scan signals from a gate driver, said drive circuit comprising adjusting means for adjusting potential differences between the pixel electrodes and a common electrode, wherein the adjusting means is composed of voltage level altering means for shifting voltage levels of the source signals supplied by the source driver equally for all the pixel electrodes, wherein the voltage level altering means is provided in a reference voltage generator means for producing source driver reference voltages from which the source driver produces the voltage levels of the source signals and is composed of: voltage divider means for producing and supplying the source driver reference voltages by voltage division of a difference between high and low reference voltages; high-and-low-reference-voltage-interconnecting means for altering the high and low reference voltages in an interconnected manner; and low-reference-voltage-specifying means for specifying a ratio of the low reference voltage to the high reference voltage.

2. (Original) The drive circuit as defined in claim 1, wherein the adjusting means adjusts the potential differences between the pixel electrodes and the common electrode to compensate for effects of variations in drain voltages caused by parasitic capacity in the thin film transistors.

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